

ABSTRACT OF THE INVENTION

A bus architecture arrangement is provided. Embodiments provide for a point-to-point protocol of a bused system, such as a processor-based system. Further embodiments may comprise a dynamically configurable point-to-point communication array with connectors and/or translators to couple hub devices with endpoint devices. Some of the connectors and/or translators may inductively or magnetically couple endpoint devices to and decouple endpoint devices from point-to-point communication media to facilitate efficient use of a point-to-point communication array.

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